

ICS4U-E		Royal St. George's College		2023/2024
Kit Name:		AVR Optimization		8 KITS
Quantity	Line #	Part	Project	Code/Note
1	1	ABRA-48 Breadboard-3220 Tie Points	CHUMP	ABRA-48
1	2	5V DC Adapter 2A	CHUMP	276-ADA (5V 2A)
1	3	HOOK-UP WIRE SPOOL SET	CHUMP	22HW-25-KIT
1	4	IC Puller	CHUMP	ICP-20
1	5	ABRA UNO R3 Compatible	*	ABRAUNO
1	6	Arduino Nano Compatible v3 ATMEGA328P	*	ARD-NANO
1	7	ALU:74LS181	CHUMP	74LS181
2	8	EEPROM:AT28C16	CHUMP	28C16A-15
2	9	(Logic) NAND:74LS00	CHUMP	74LS00
2	10	(Logic) NOT:74LS04 (Inverter for RAM)	CHUMP	74LS04
2	11	(Logic) AND:74LS08 (Branch Control)	CHUMP	74LS08
2	12	(Logic) OR: 74LS32 (Clock)	CHUMP	74LS32
1	13	MUX/SEL: 74LS157	CHUMP	74LS157
1	14	PC: 74LS161	CHUMP	74LS161
1	15	ADDRESS: 74LS174	CHUMP	74LS174
1	16	ACCUM: 74LS377	CHUMP	74LS377
1	17	RAM: 74189	CHUMP	74189
4	18	Clock: LM555	CHUMP	LM555
6	19	Clock: Momentary Tactile Button (12mm)	CHUMP	PBS-190
1	20	Clock: 0.1uF Capacitor	CHUMP	753-ADA
4	21	Clock: 1uF Capacitor	CHUMP	1R50
4	22	Clock: SPDT Slide Switch	CHUMP	SSW-120-BB
20	23	LED 3mm: Red	CHUMP	LED-3R
20	24	LED 3mm: Amber	CHUMP	LED-3A
20	25	LED 3mm: Green	CHUMP	LED-3G
4	26	ATTiny84-20PU 8-Bit Microcontroller AVR	Dolgin Dev Board v6	ATTINY84-20PU
8	27	Female 1 row X 8 pins Header	Dolgin Dev Board	FH-1x8
10	28	16SG Socket IC, Dual contact, DIP16, Gold pins	Dolgin Dev Board	16SG
2	29	Panel Mount DC Power Jack 2.1mm	Dolgin Dev Board	610-ADA
2	30	Right Angle Shrouded Male 6-pin ISP Header	Dolgin Dev Board	732-5404-ND
5	31	Transistor Positive Voltage Regulator 1A 5V	Dolgin Dev Board	7805T
20	32	2N3904 BJT	Dolgin Dev Board	2N3904
5	33	1x40 pins Straight Male Snappable	DDB Intersection	SH-6A-M
5	34	CMOS BCD-7-segment Latch	DDB Intersection	4511
2	35	CD4511 BCD Decoder	DDB ADC	74HC595
2	36	Female Header: 2x3		S7106-ND